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PTO/SB/108 (8-98)

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# Declaration and Power of Attorney For Patent Application

#### 特許出願宣言書及び委任状

#### Japanese Language Declaration

#### 各官宜韶本日

下記の氏名の発明者として、私は以下の通り宜言します。

As a below named inventor, I hereby declare that:

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My residence, pest office address and citizenship are as stated next to my name.

I believe I am the original, first and sole inventor (if only one

name is listed helow) or an original, tirst and joint inventor (if

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ulaimed and for which a patent is snught on the invention

下記の名称の発明に関して請求範囲に記載され、特許出願して いる発明内容について、私が最初かつ唯 の発明者(下記の氏名 が つの場合) もしくは最初かつ共間発明者であると (下記の名 森が複数の場合) 信じています。

半導体装置及びその製造方法、回路基板並びに電子機器

entitled SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING THE SAME CIRCUIT SUBSTRATE AND ELECTRONIC EQUIPMENT

上記発明の明部書(下記の欄で×印だっいていない場合は、不 者に添付)は、

the specification of which is attached hereto unless the following box is checked:

図 2003年11月26日に提出され、米国出願番号または **伶許協定条約 国際出職番号を 10/724,581 とし、** に訂正されました。 (政当する場合)

was filed on November 26, 2003 as United States Application Number or PCT International Application Number 10/724,581 and was amended on (if explicable).

利は、特許請求範囲を含む上記訂正後の明細書を検討し、内容 を理解していることをここに表明します。

I horoby etate that I have reviewed and understand the contents of the above identified specification, including the olsims, as amended by any amendment referred to above.

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I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.58.

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Prior Foreign Application(s) 外国での先行出顧

2002-343815 Japan (Gountry) (金号) (国名)

27/November/2002 (Day/Month/Year Filed) (出版年月日)

優先閥主張なし

(Number) (番号)

(Country) (国名) (Day/Month/Year Filed) (出數年月日)

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